



Engineering Change Order

ECO-040704

Product Line:	HW-AFX-SP3-400
Product Revision:	Rev 2
ECO:	040704
Effective Date:	04/07/04
Initiator:	Bill Pratt

1.0 ECO Description:

The HW-AFX-SP3-400 development board has a missed route between the M29W320DB (U5) Flash, Pin 45 and the XC3S400 (U1) FPGA.

U5 is implemented with a byte wide data interface, the byte wide configuration requires DQ15A-1 pin 45 of U5 to behave as an address pin when in byte wide mode. DQ15A-1 low will select the LSB of the word; on the other addresses DQ15A-1 high will select the MSB.

U5 is not addressable unless a jumper wire change is implemented on the HW-AFX-SP3-400 board.

2.0 Technical Work Required:

Installation of a jumper wire is required for correction:

Install a jumper wire from U5 Pin 45 (DQ15A-1) to J4 Expansion Connector (Header 12X2) Pin 20 (FPGA I/O 16).

U5 Pin 45 (DQ15A-1) will be routed to the XC3S400 (U1) Pin 175, allowing proper interface to the M29W320DB Flash (U5) in byte wide mode.

Please refer to Figure 1. for detailed jumper installation.

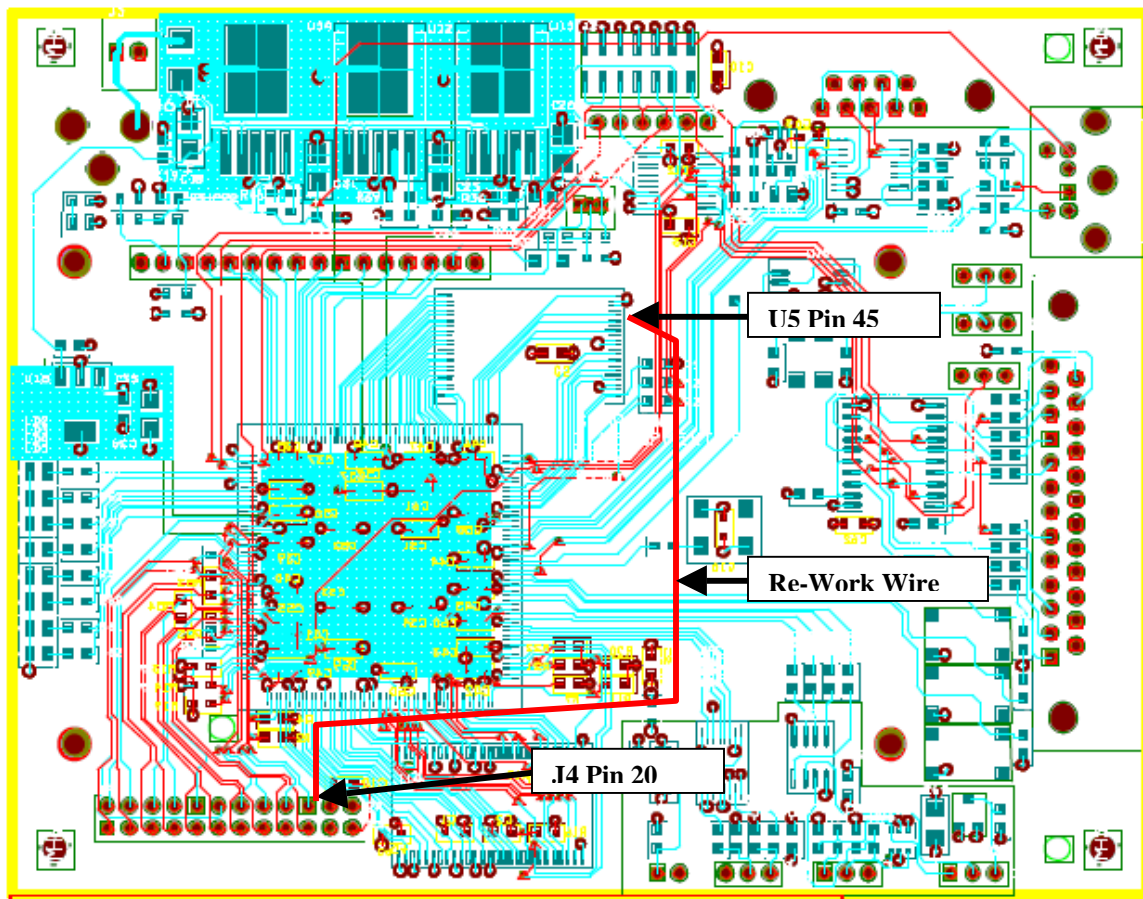


Figure 1.



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3.0 Product Return Procedure:

Customers that choose to have the boards returned to Nu Horizons Electronics for execution of ECO-040704, follow Nu Horizons Electronics RMA process.

Boards will be replaced with Revision 3 with scheduled shipment of 04/26/04.

Nu Horizons Electronics apologizes for the inconvenience, and will do all that is required to return your board to you in a timely fashion.